	В	Brief Profile		
Name	:	Abhishek Kumar		
Date of Birth	:	10 August 1995		
Educational Qualification • Ph.D.	:			
•M. Tech	:	Mtech from NIT uttrakhand		
•B.Tech	:	Btech Hons from AKTU		
Work Experience				
• Teaching		1 year in SDBIT college , deh		
• Research / Industry	:	2 months in engineer succes	ss coaching institue a	s a faculty for GATE
E-mail ID	:	abhishek.kumar.ee@miet.ac.in		
Contact No.	:	9555199956		
Area of Interest	:	Networks Analysis, Control Systems and Power Electronics		
Teaching				
• Subjects Taught at UG Level		Basic Electrical, Netwoks An	alysis, Analog Electro	onics and Power
• Subjects Taught at PG Level	:	Electronics		
Research Guidance				
•B.Tech	:			
•M. Tech				
• Ph.D.	:			
Research Publications				
• Journals	:			
• Conferences	:			
• Book Chapters	:	2 research paper		
		and 1 book chapter		
Patent/IPR	:			
(Books Published etc.)				
No. of National/International Conferences attended/ Paper Presented		3		
No. of Conferences Organized				
STC/FDP/Seminars/Workshops Organized				
STC/FDP/Summer/Winter Schools/Workshops /Seminars attended				
Certification Courses (NPTEL etc.)				
Memberships of the Professional Societies	s :			
Awards/Honors	:	Gate qualified, MHRD fellowship, BTech hons		
Funded Project	:	1. A.		T
Name of Project	F	unding Agency	Duration	Total Value
Any other relevant Information	•			

LIST OF PUBLICATIONS

Journal:
[1] [2] [3]
Books / Book Chapters:
[1] Abhishek Kumar, Durgesh Chandra Nautiyal and Prakash Dwivedi "Control Applications in Modern Power System: Closed Loop Control of Non-ideal Buck Converter with Type-III Compensator" Springer Nature Singapore Pte Ltd. 2021 [2] [3]
Conferences:
[1]" Design and Analysis of Buck Converter using Sliding Mode Approach ", IEEE, SCES'20, India
2} " Design and Analysis of Boost Converter using Sliding Mode Approach ", IEEE, IECON'20, Singapore